



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2183

SERIAL NUMBER 10/086,717	FILING DATE 03/04/2002  RULE	CLASS 438	GROUP ART UNIT 2891	ATTORNEY DOCKET NO. H-1034
-----------------------------	---------------------------------------	--------------	------------------------	----------------------------------

## APPLICANTS

Shigeru Nakamura, Ayase, JAPAN;

Masakatsu Gotou, Atsugi, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-012775 01/22/2002

JAPAN 2001-108603 04/16/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 03/21/2002

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 39	TOTAL CLAIMS 43	INDEPENDENT CLAIMS 11
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> met after Allowance				
Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>				

## ADDRESS

24956

MATTINGLY, STANGER, MALUR &amp; BRUNDIDGE, P.C.

1800 DIAGONAL ROAD

SUITE 370

ALEXANDRIA, VA

22314

## TITLE

Semiconductor device and method of manufacturing the same

FILING FEE  RECEIVED 1826	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
------------------------------------	---	--

☐ Credit

**THIS PAGE BLANK (USPTO)**